

FORM PTO-1449 <u>INFORMATION DISCLOSURE STATEMENT</u>			ATTY. DOCKET NO. 1875.3770001		APPLICATION NO. (to be assigned) (Continuation of U.S. Appl. No. 10/440,311)		
			APPLICANTS BURNS <i>et al.</i>				
			FILING DATE Herewith	GROUP 10-Be Assigned		28-39	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
AA1							
AB1							
AC1							
AD1							
AE1							
AF1							
AG1							
AH1							
AI1							
AJ1							
AK1							
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
AL1							Yes No
AM1							Yes No
AN1							Yes No
AO1							Yes No
AP1							Yes No
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>DR</i>	AR	1	Allen, P. and Holberg, D., <i>CMOS Analog Circuit Design</i> , Saunders College Publishing, 1987, pages 58-66 and 76-86.				
<i>DR</i>	AS	1	Johns, D. and Martin, K., <i>Analog Integrated Circuit Design</i> , John Wiley & Sons, Inc., 1997, pages 16-39 and 57-60.				
	AT	1					
EXAMINER <i>Jimmy Neary</i>				DATE CONSIDERED <i>7/23/04</i>			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicants.							